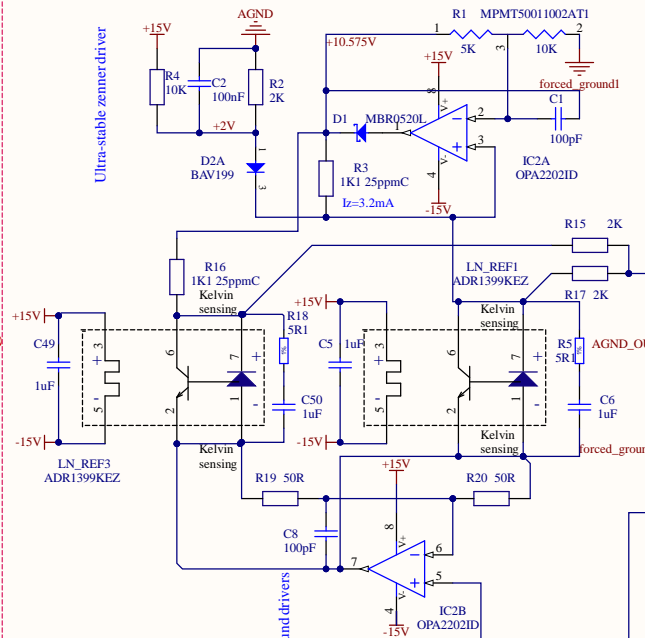
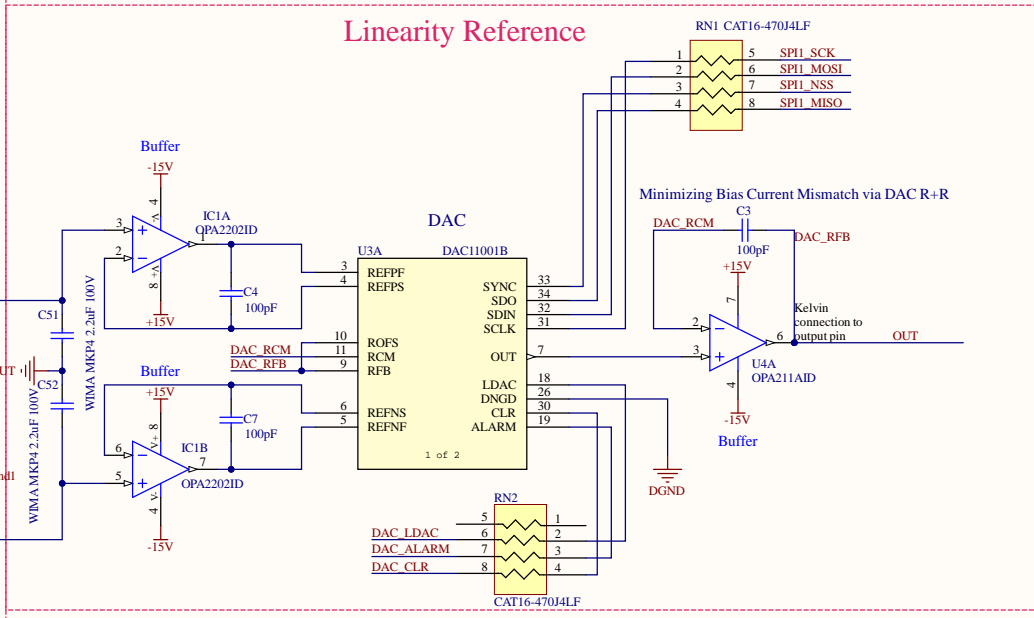


DAC board

Voltage low noise Reference

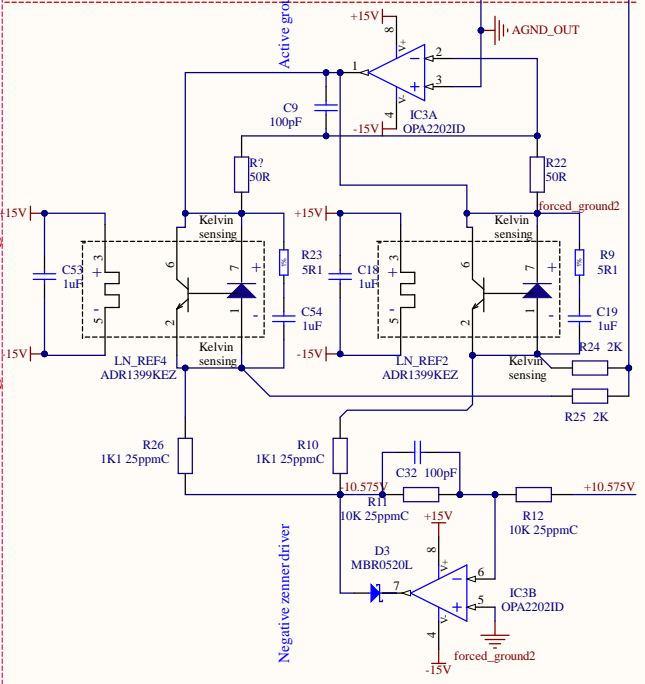


Linearity Reference

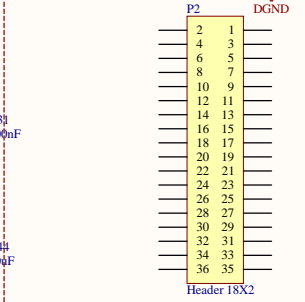
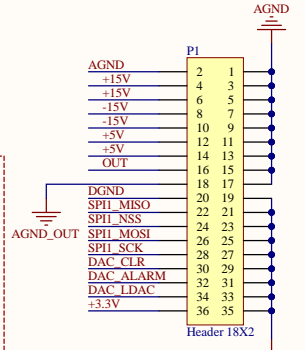
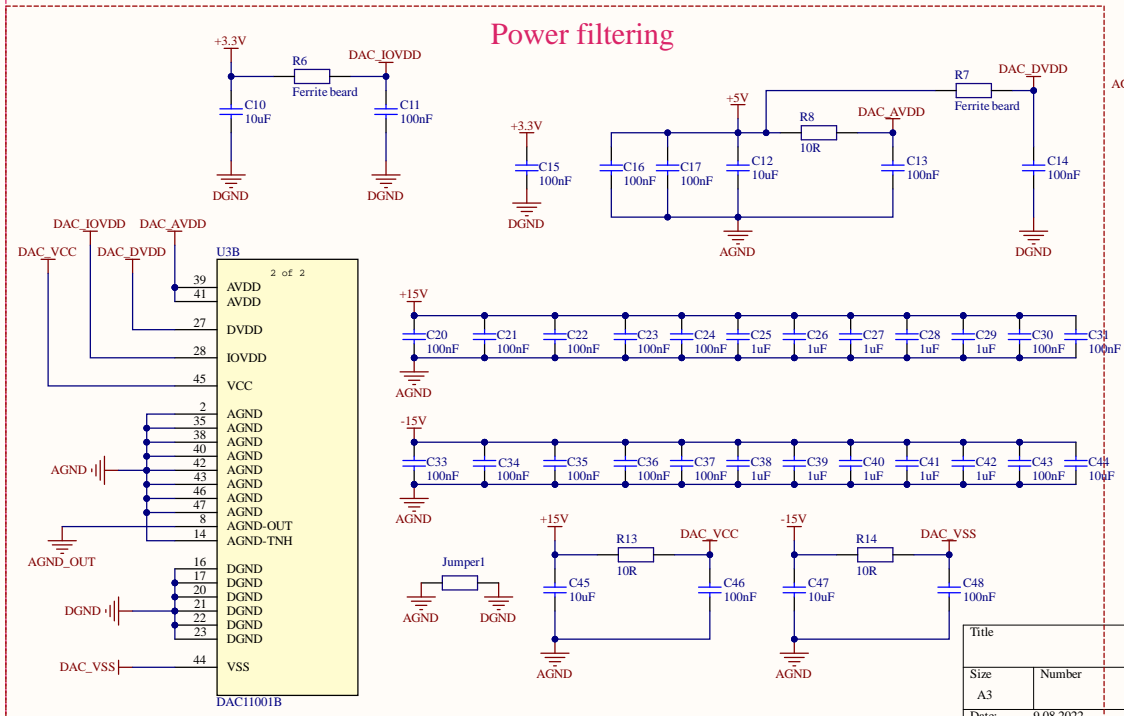


Positive voltage source

Negative voltage source



Power filtering



Size	Number	Revision
A3	9.08.2022	Sheet of
File: C:\I\projects\DAC_board_schematic.Sch		Drawn By: