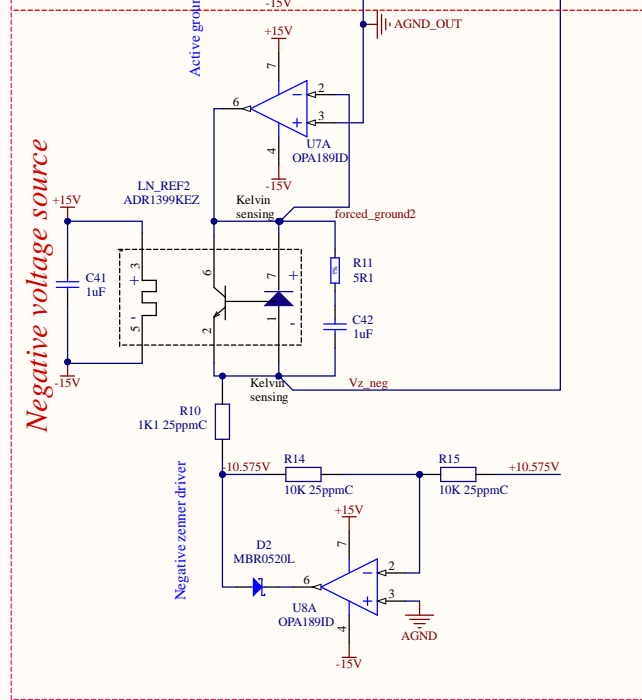
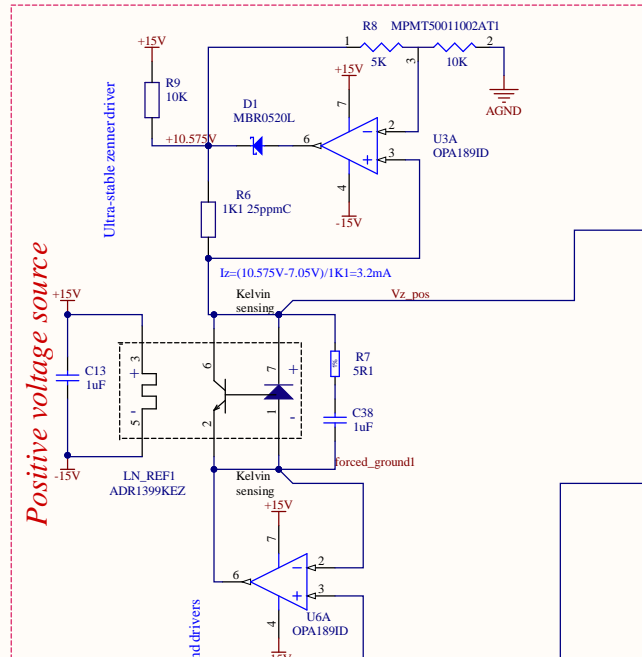
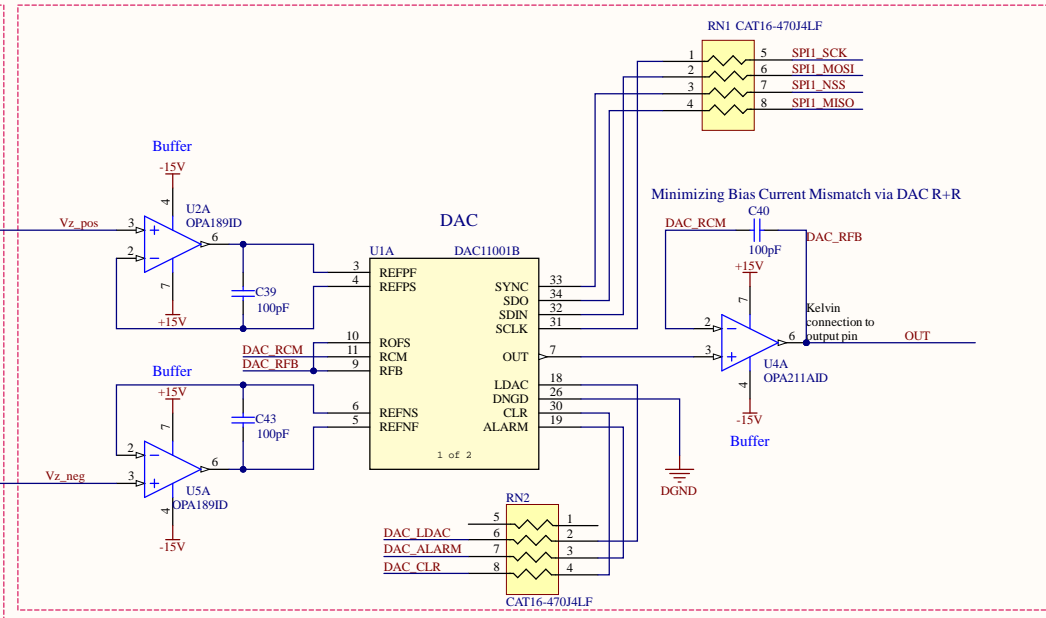


DAC board

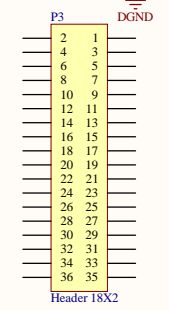
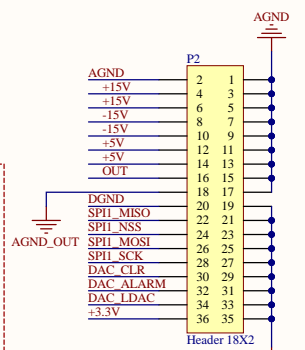
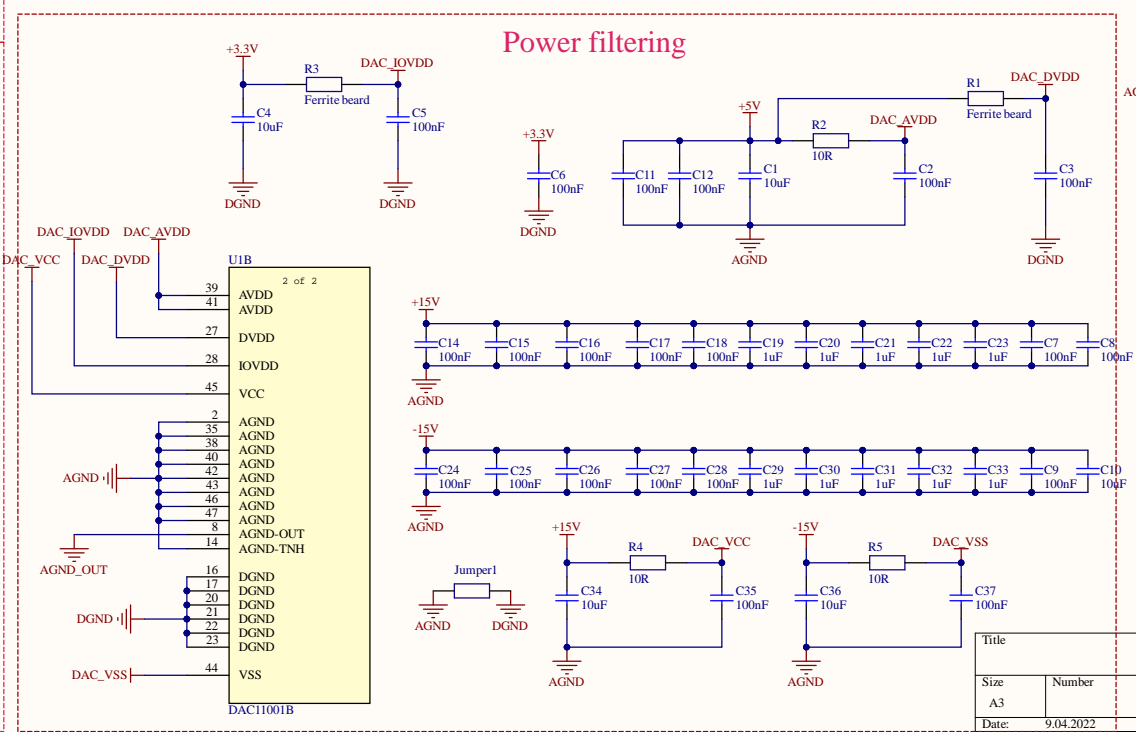
Voltage low noise Reference



Linearity Reference



Power filtering



Size	Number	Revision
A3		
Date:	9.04.2022	Sheet of
File:	C:\Ipoecrta\dac_board_schematic.Sch	Drawn By: